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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/520,805	10/13/2005	Daniel Kehrer	1432.113.101/P29564	9933
25281 7590 09/10/2008 DICKE, BILLIG & CZAJA FIFTH STREET TOWERS 100 SOUTH FIFTH STREET, SUITE 2250 MINNEAPOLIS, MN 55402				
EXAMINER				
MIS, DAVID C				
ART UNIT		PAPER NUMBER		
2817				
MAIL DATE		DELIVERY MODE		
09/10/2008		PAPER		

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary

Application No.

10/520,805

Applicant(s)

KEHRER ET AL.

Examiner

David Mis

Art Unit

2817

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 13 October 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 2-23 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 2-8, 10, 12-17 and 20-23 is/are rejected.
- 7) ☒ Claim(s) 9, 11, 18 and 19 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 10 January 2005 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☒ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date See Continuation Sheet
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date _____
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: _____

Continuation of Attachment(s) 3). Information Disclosure Statement(s) (PTO/SB/08), Paper No(s)/Mail Date : 0407 / April 7th, 2005.

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-5, 7, 8, 12-14 and 23 are rejected under 35 U.S.C. 102(b.) as anticipated by or, in the alternative, under 35 U.S.C. 103(a) as obvious over Vajdic et al (4,791,326).

Vajdic et al disclosed an integrated circuit arrangement (column 2, lines 3-5) comprising an output circuit (Fig. 8 showing one output buffer; and where the "output circuit" is considered to be the array of such output buffers of the semiconductor circuit) having at least one first output connection (OUT, 105) which can provide a data signal (from the semiconductor devices functional elements); at least one first data output connection (PAD, 93); at least one first inductance (92) connected between the at least one first output connection and the at least one first data output connection; wherein the output circuit has a second output connection (a respective OUT node of the output buffers of the array) and a second data output connection (a respective PAD); at least one second inductance (a respective inductance like 92) connected between the second output connection and the second data output connection; comprising where the first inductance is in a form such that it forms a first frequency filter having a prescribed frequency band together with the first data output connection (92 and 94 form a filter that necessarily has a band), and the second inductance is in a form such that it forms a

second frequency filter having the prescribed frequency band together with the second data output connection (a respective filter and band); comprising where the prescribed frequency band is in a range from 1 GHz to 100 GHz (necessarily provided a range corresponding to the data frequency; and alternatively it would have been obvious for one of ordinary skill in the art to have incorporated a band corresponding to the data frequency, "motivated" to provide the data); comprising where the output circuit is set up such that a differential data signal can be provided at the first output connection and at the second output connection (operation of the array of output buffers did not prevent outputting differential data signals); comprising where the at least one first inductance is coupled to the at least one second inductance (electromagnetic coupling of some magnitude existed).

3. Claims 20-23 are objected to because of the following informalities: The claims are objected to because claim 20 is redundant with claim 15 (except for "first" in line 6 of claim 15 and not in line 6 of claim 20 - which does not seem to mean anything different); and because claim 21 is redundant with claim 16; and because claim 22 is redundant with claim 18. Appropriate correction is required.

4. Claims 1-8, 10, 12-17 and 20-23 are rejected under 35 U.S.C. 102(b.) as anticipated by or, in the alternative, under 35 U.S.C. 103(a) as obvious over Miller (WO 00/51012).

Miller disclosed an integrated circuit arrangement (Title) comprising an output circuit (Figs. 7-11 showing one output buffer; and where the "output circuit" is considered to be the array of such output buffers of the semiconductor circuit) having at least one first output connection (from DRV 70) which can provide a data signal (from the semiconductor devices functional elements); at least one first data output connection (pin or leg: 86); at least one first inductance (88A, 88B) connected between the at least one first output connection and the at least one first data output connection; wherein the output circuit has a second output connection (a respective DRV node of the output buffers of the array) and a second data output connection (a respective pin or leg); at least one second inductance (a respective inductance like 88A, 88B) connected between the second output connection and the second data output connection; comprising where the first inductance is in a form such that it forms a first frequency filter having a prescribed frequency band together with the first data output connection (C1DRV and 88A form a filter that necessarily has a band), and the second inductance is in a form such that it forms a second frequency filter having the prescribed frequency band together with the second data output connection (a respective filter and band); comprising where the prescribed frequency band is in a range from 1 GHz to 100 GHz (necessarily provided a range corresponding to the data frequency; and alternatively it would have been obvious for one of ordinary skill in the art to have incorporated a band corresponding to the data frequency, "motivated" to provide the data); comprising a plurality of frequency filters coupled in series between the at least first output connection and the at least first data output connection (C1DRV and 88A formed a first filter;

C1ESD and 88B formed a second filter in series with the first filter; comprising where the output circuit is set up such that a differential data signal can be provided at the first output connection and at the second output connection (operation of the array of output buffers did not prevent outputting differential data signals); comprising where the at least one first inductance is coupled to the at least one second inductance (electromagnetic coupling of some magnitude existed); comprising where the output circuit has a differential amplifier (Miller disclosed output driver DRV generally, which corresponded to a differential arrangement as well as any other; alternatively it would have been obvious to one of ordinary skill in the art to have incorporated a differential amplifier in DRV, "motivated" to provide data).

5. Claims 9, 11, 18 and 19 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to David Mis whose telephone number is (571) 272-1765. The examiner can normally be reached on Monday through Thursday; 6-11 AM and 12-5 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (571) 272-1769. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/David Mis/
Primary Examiner, Art Unit 2817